LSSD-COMPATIBLE EDGE-TRIGGERED SHIFT REGISTERED LATCH

BUR920020128US1

ASHTON ET AL.

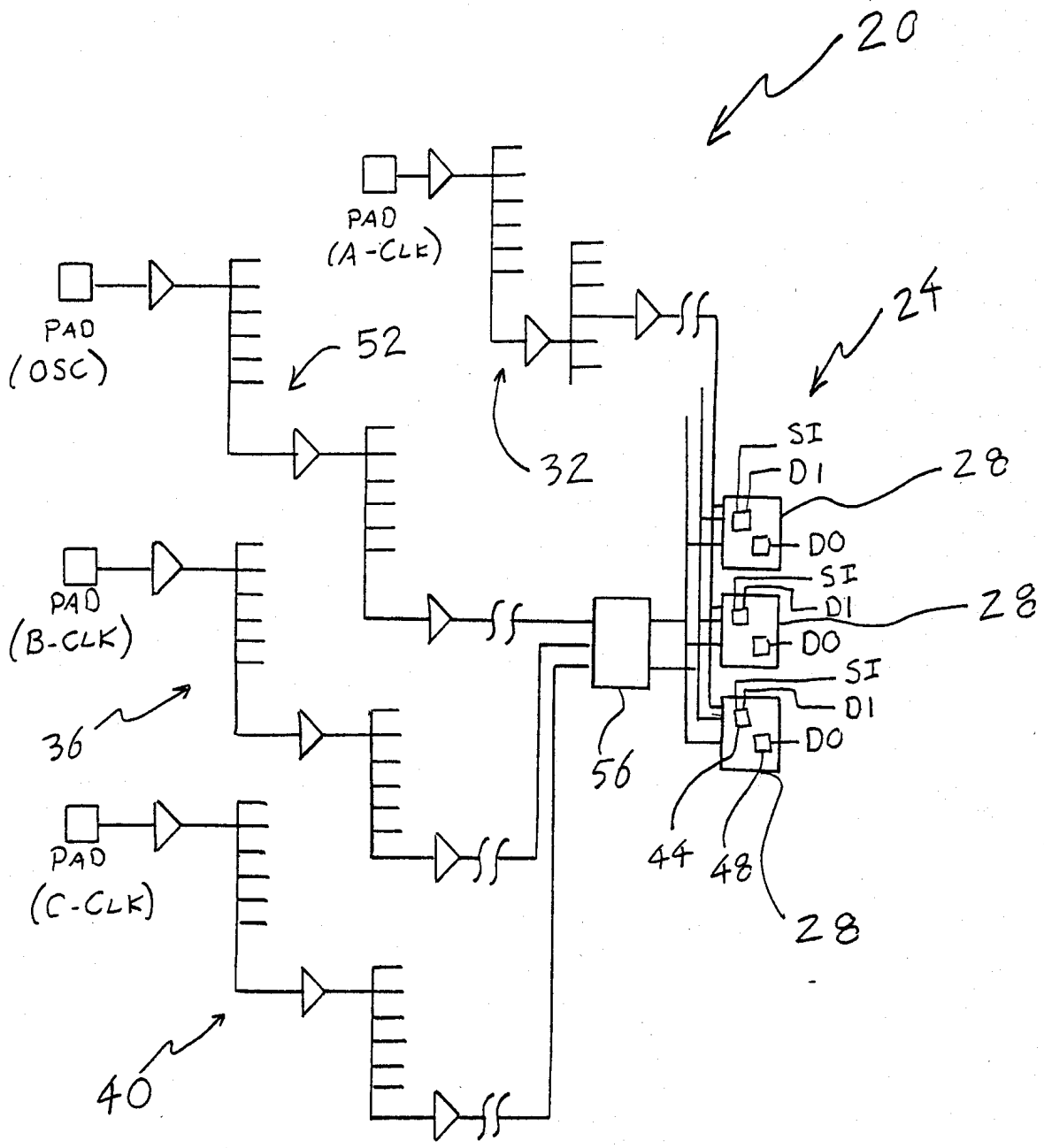

FIG 1

(PRIOR ART)

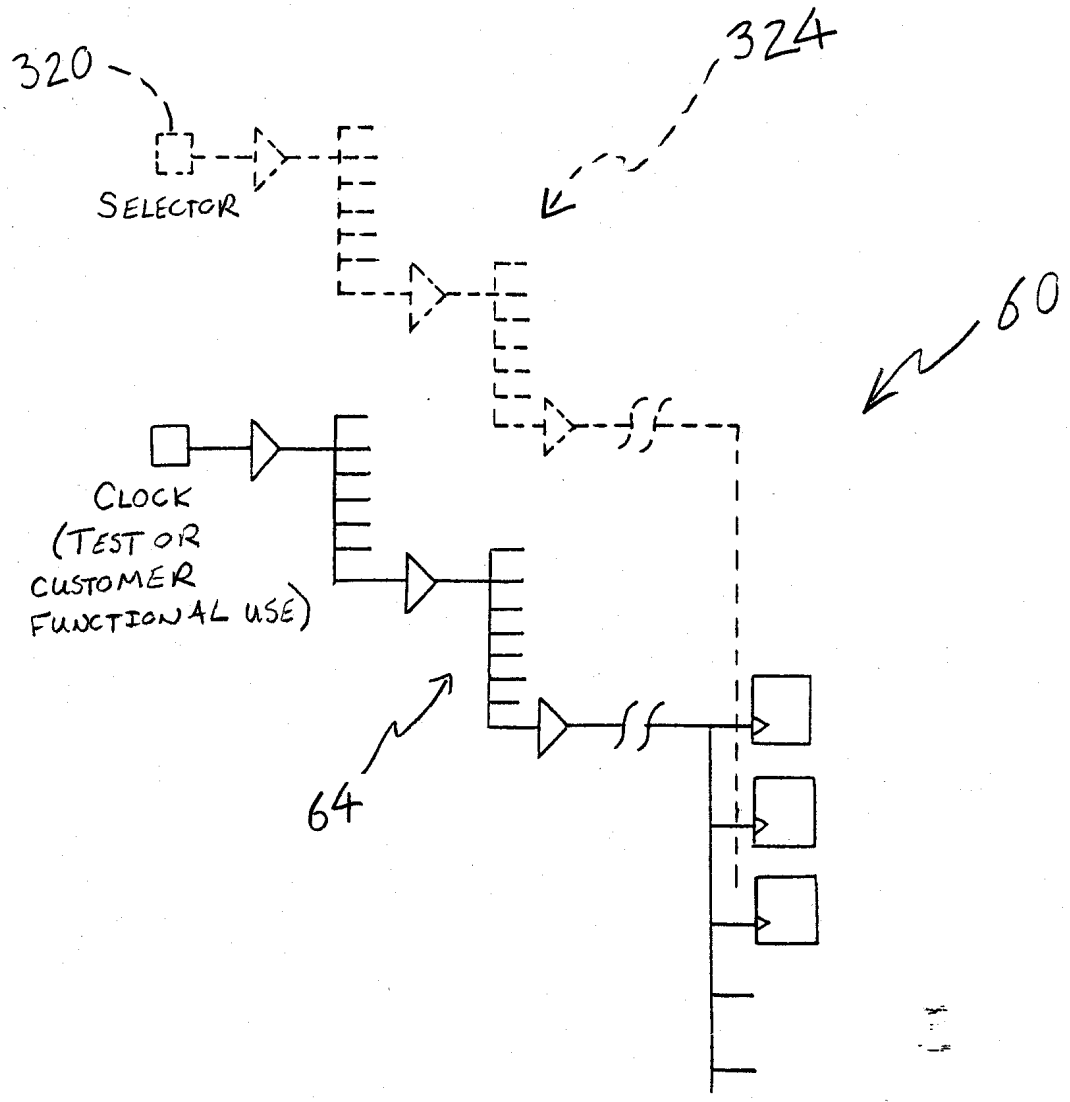
320
324
SELECTOR
60
CLOCK
(TEST OR
CUSTOMER
FUNCTIONAL USE)
64

FIG. 2

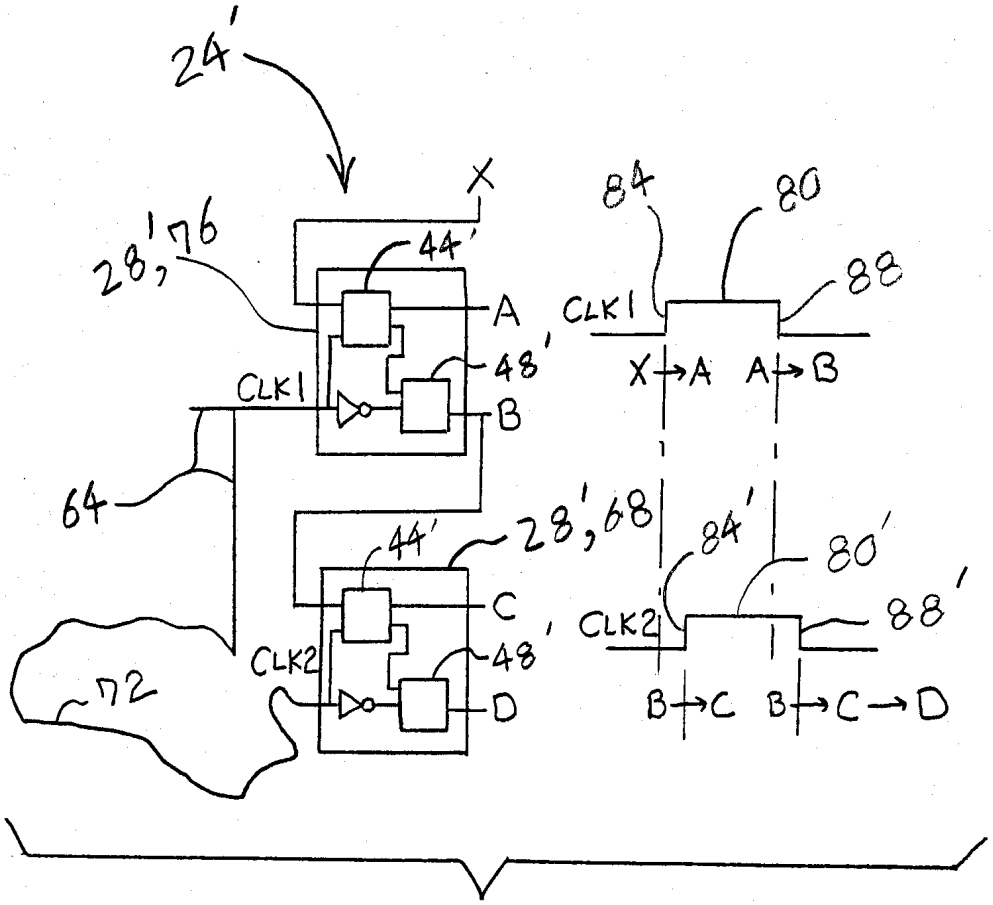

FIG. 3
(PRIOR ART)

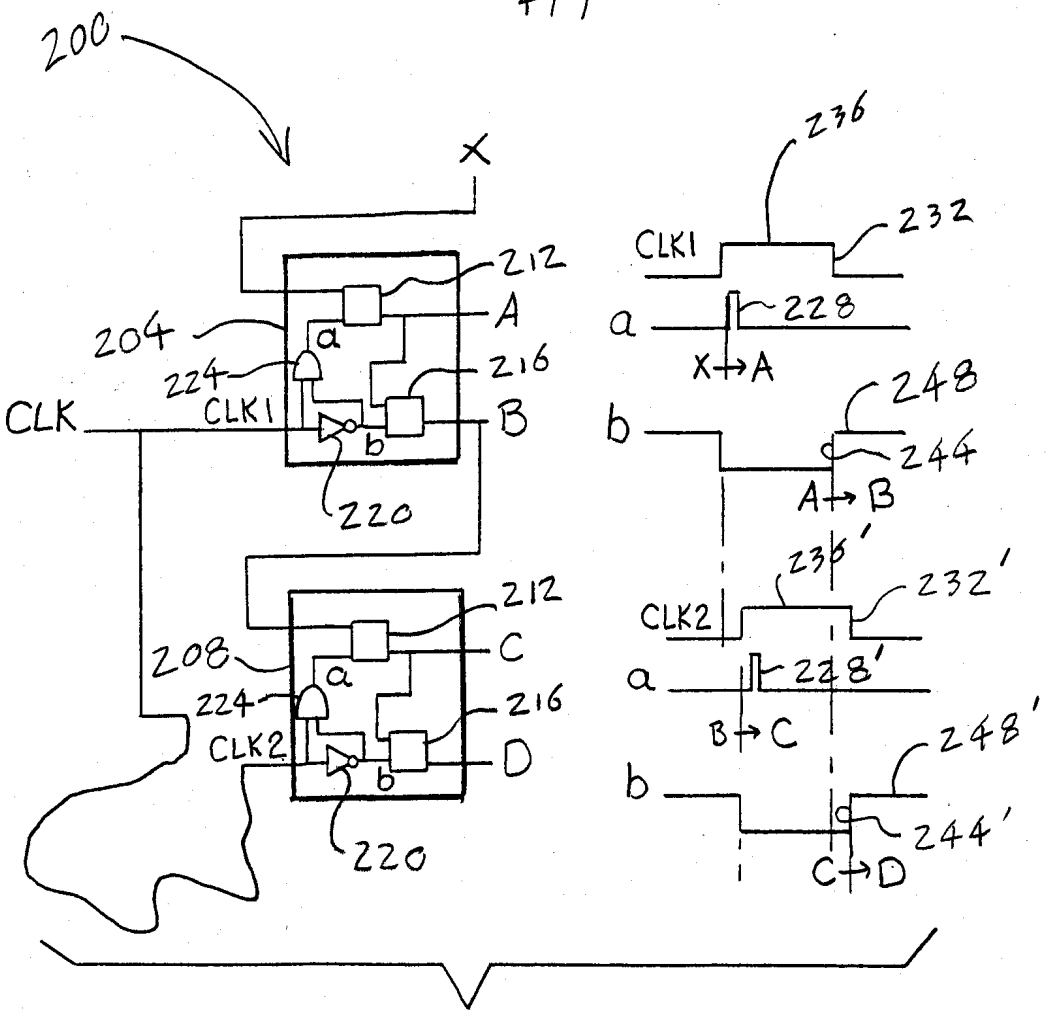

FIG. 4

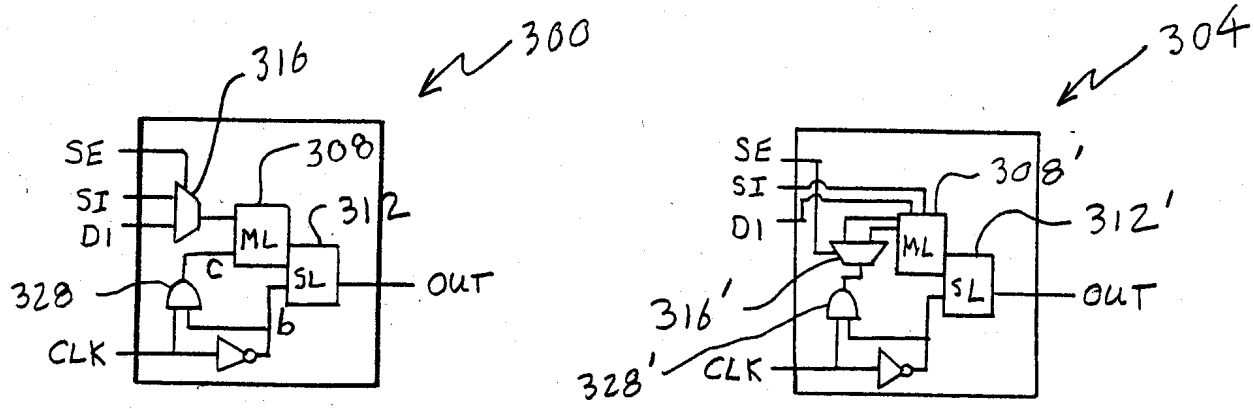

FIG 5A

FIG. 5B

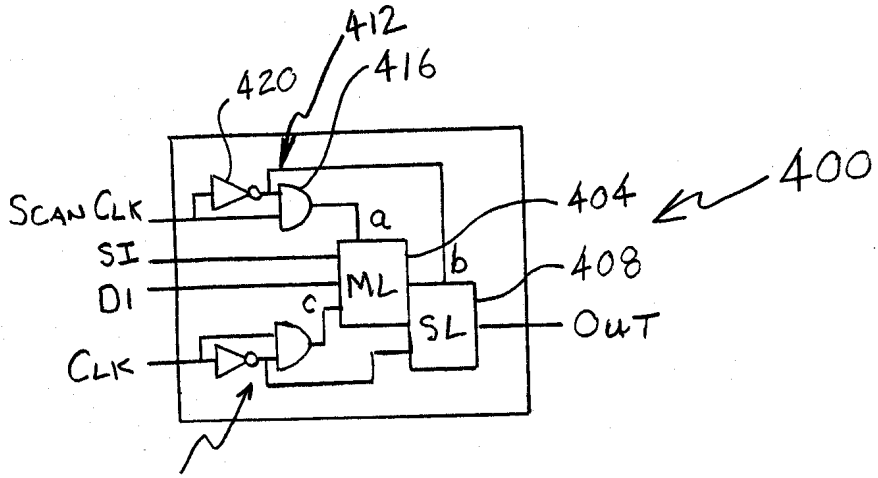

FIG. 5C

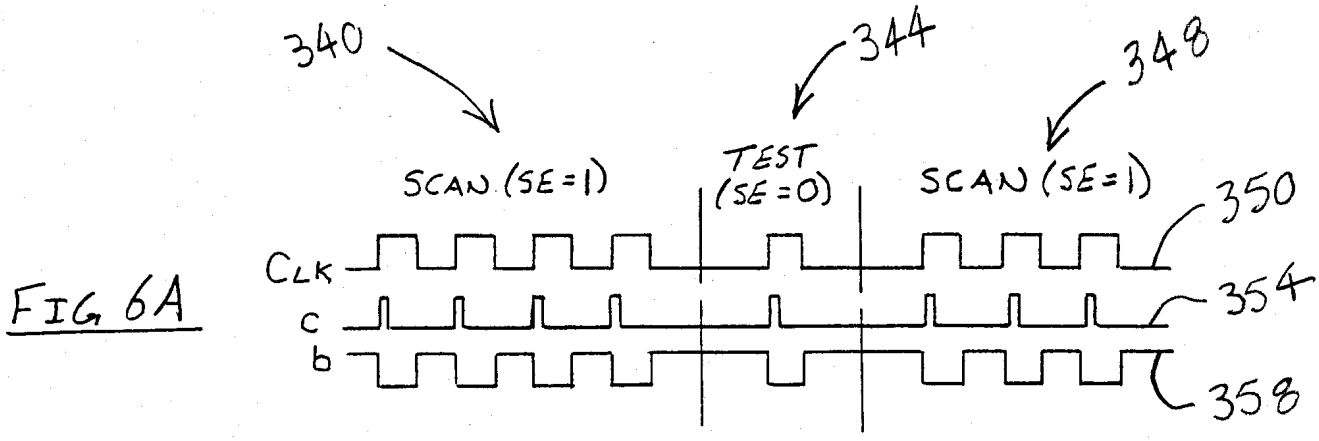

FIG 6A

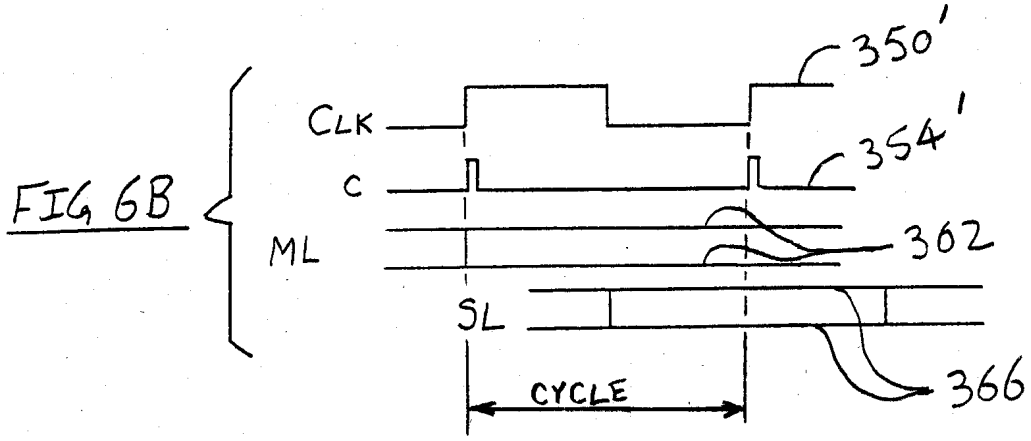

FIG 6B

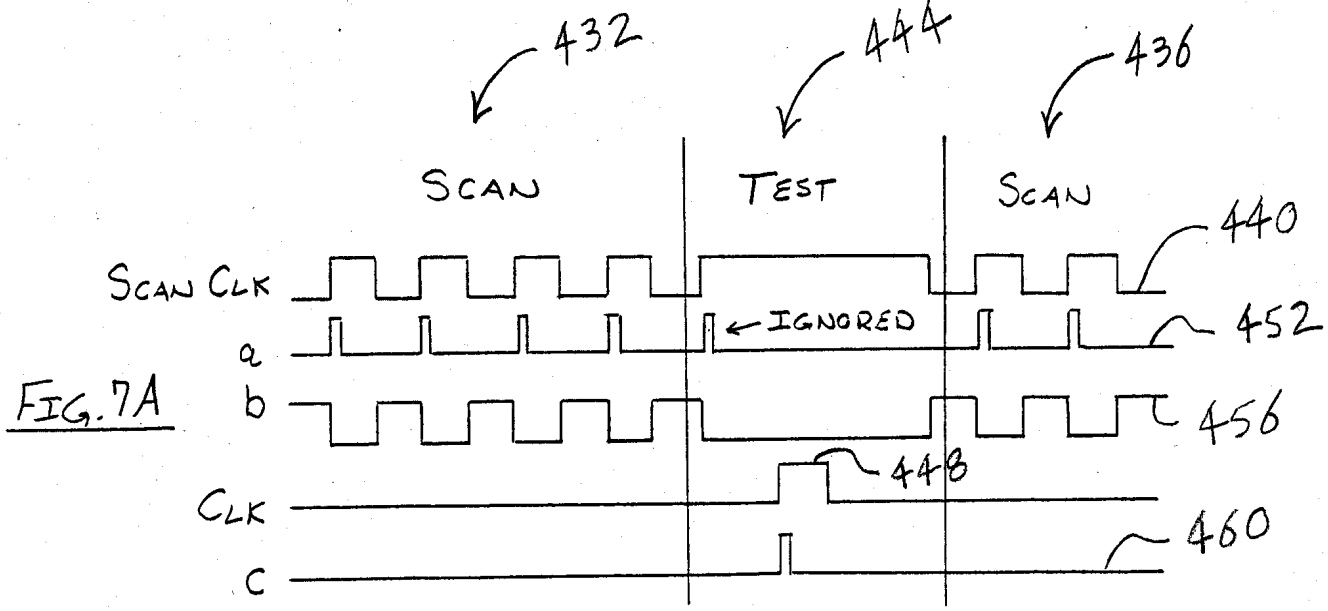

FIG. 7A

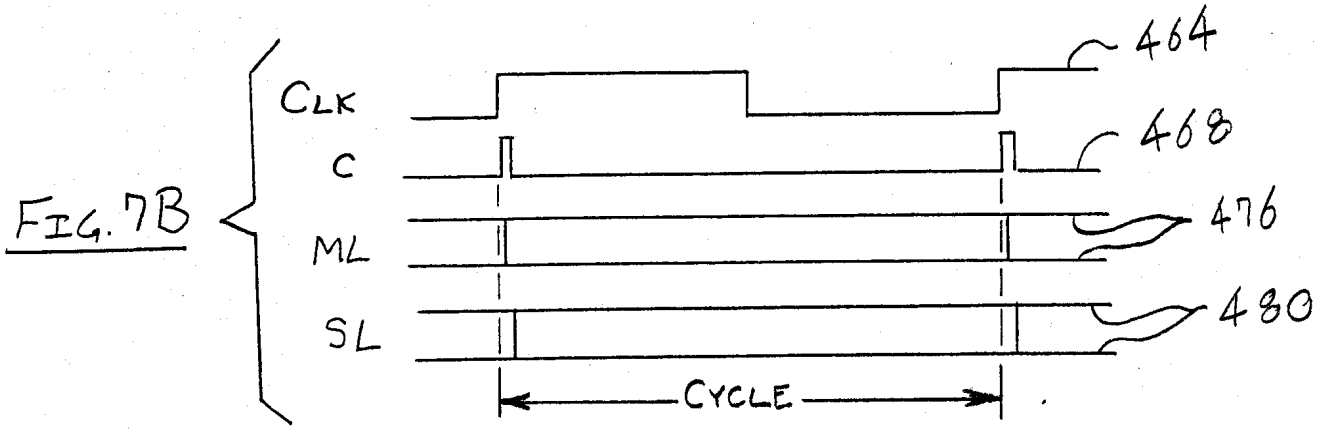

FIG. 7B

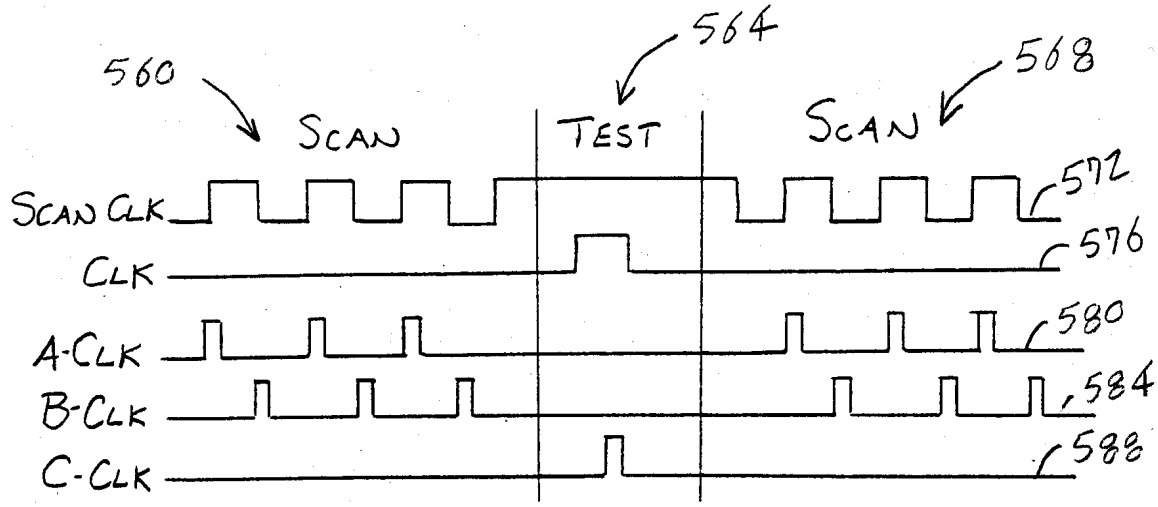

FIG 8B

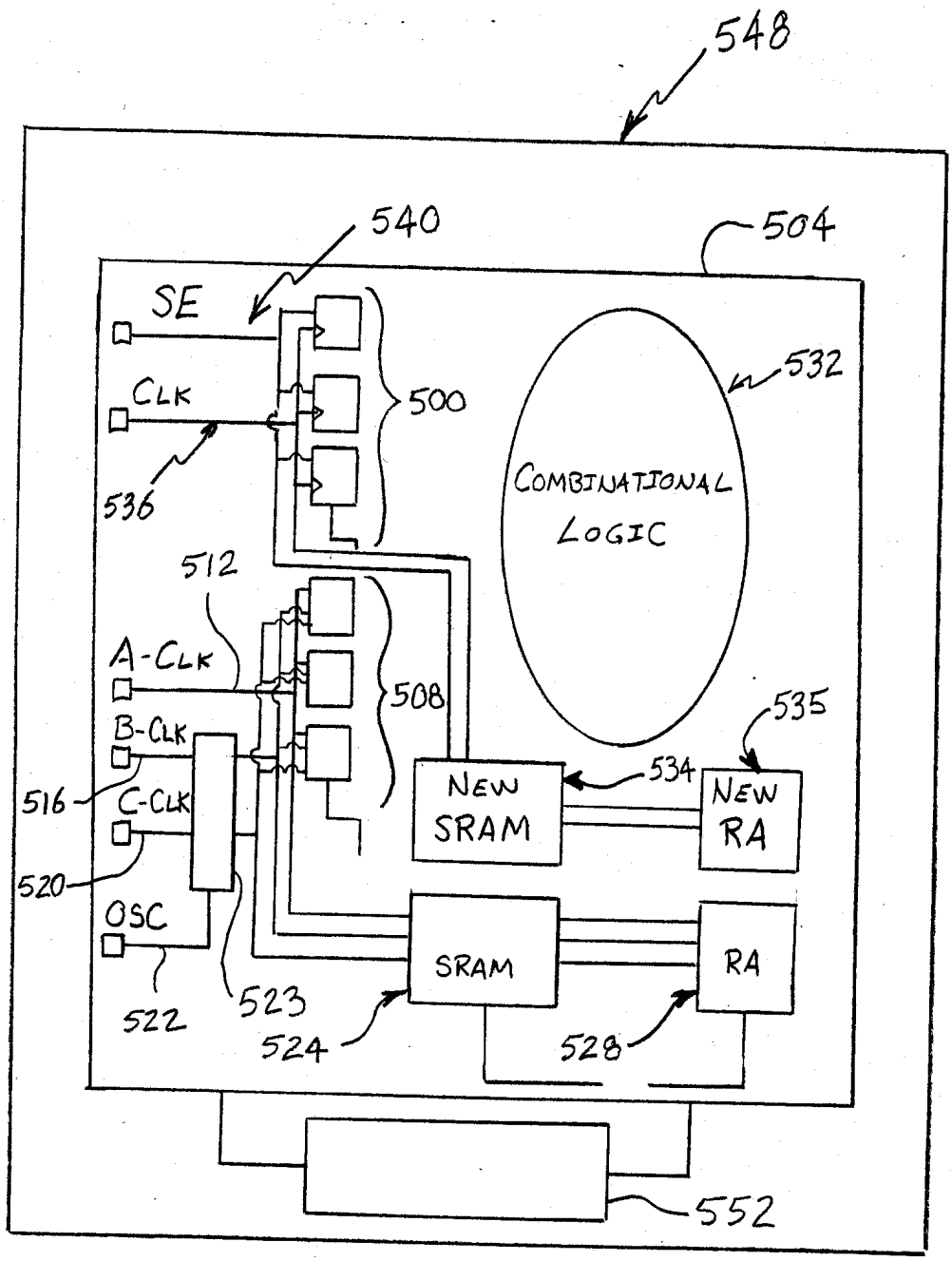
548
540
504
SE
CLK
536
500
532
COMBINATIONAL LOGIC
512
A-CLK
508
B-CLK
516
C-CLK
520
OSC
522
523
524
NEW SRAM
534
NEW RA
535
SRAM
RA
528
552

FIG. 8A